

Notice of Allowability	Application No.	Applicant(s)	
	10/689,099	KANAYAMA ET AL.	
	Examiner Steven H. Rao	Art Unit 2814	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 3/28/05.
2. The allowed claim(s) is/are 1-12.
3. The drawings filed on 21 October 2003 are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

Response to Amendment

Applicants' amendment filed on March 17, 2005 has been entered and forwarded to the examiner on March 28, 2005.

Therefore claims 1-3 as amended by the amendment and claims 4-9 as previously recited and claims 10-12 as presently newly added are currently pending in the Application.

Allowable Subject Matter

Claims 1-12 are allowed.

The following is an examiner's statement of reasons for allowance:
The prior art taken either singularly or in combination fails to anticipate or fairly suggest the limitation of the dependent claims, in such manner that a rejection under 35 U.S.C. 102 or 103 would be proper. The applied prior art fails to teach a combination of all the claimed features as presented in independent claims, which include an electronic control device as claimed in independent claim 1 namely in an electronic control device having a power supply circuit formed on a multi-layered substrate for adjusting a power supply voltage supplied by an external power supply to a desired level, the power supply circuit including a switching component to which the power supply voltage is applied at an input terminal thereof and which is driven by a duty signal including an output voltage forming circuit formed in a top layer of the multi-layered substrate for producing an output voltage formed from a voltage inputted from the switching device; a ground wiring pattern that defines a ground electrical potential of the output voltage forming circuit; a common ground wiring pattern that defines a ground electrical

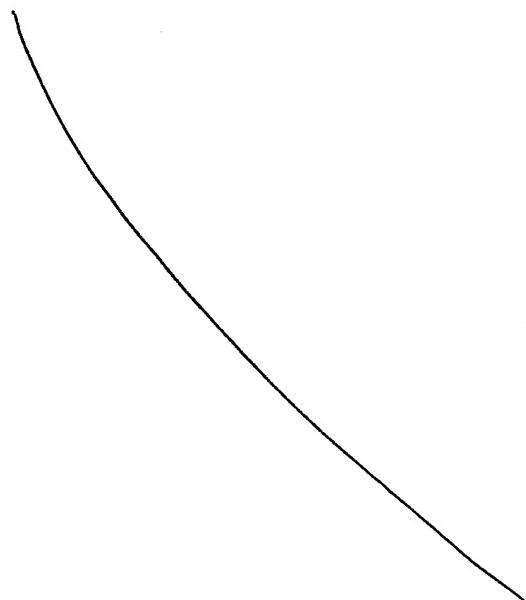
Art Unit: 2814

potential of each of electronic circuits included in the electronic control devices except for the output voltage forming a circuit, and a connecting part that connects the ground wiring pattern with the common ground wiring pattern, wherein the ground wiring pattern is formed in a layer of the multi-layered substrate and connected to the output voltage forming circuit, and the common ground wiring pattern is formed in a layer of the multi-layered substrate, and especially the prior art of record does not disclose ground wires being formed in the layer of the multilayer substrate.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven H. Rao whose telephone number is (571) 272-1718. The examiner can normally be reached on 8.00 to 5.00.

The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.



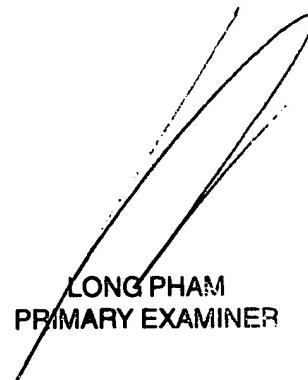
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Art Unit: 2814

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Steven H. Rao
Patent Examiner

June 09, 2005.


LONG PHAM
PRIMARY EXAMINER